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|  | **Computer Science Python** | **Computer Science Java** | **Cisco I** | **Cisco II** |
| **Mon****27** | \*Debugging Interlude 1 | Sem 1 Chapter 3 ProgramsSem 2 Chapter 7 Programs Sem 3 Chapter 10 ProgramsSem 4 Chapter 13 ProgramsSem 5 Chapter 16 ProgramsSem 7 Present Topic #2  | \*PT Lab 11.7.5 – Subnetting Scenario | Module 13 Work |
| **Tues****28** | \*Functions Section 1 | Sem 1 \*Chapter 3 TestSem 2 Chapter 7 ProgramsSem 3 Chapter 10 ProgramsSem 4 Chapter 13 ProgramsSem 5 Chapter 16 ProgramsSem 7 Own Topic Program 2  | Finish Lab 11.7.5 | Module 13 Work |
| **Wed 1** | \*Functions Section 2 | Sem 1 \*Chapter 3 Test CorrectionsSem 2 Chapter 7 ProgramsSem 3 Chapter 10 ProgramsSem 4 Chapter 13 ProgramsSem 5 Chapter 16 ProgramsSem 7 Own Topic Program 2  | \*PT Lab 11.9.3 – VLSM Design and Implementation Practice | Module 13 Work |
| **Thurs 2** | \*Functions Section 3 | Sem 1 \*Chapter 3 Test CorrectionsSem 2 Chapter 7 Programs Sem 3 Chapter 10 ProgramsSem 4 Chapter 13 ProgramsSem 5 Chapter 16 Programs Sem 7 Own Topic Program 2  | Finish Lab 11.9.3 | Module 13 Work |
| **Fri** **3** | Finish Functions Sections | Sem 1 Chapter 4 MC & YDIsSem 2 Chapter 7 ProgramsSem 3 Chapter 10 ProgramsSem 4 Chapter 13 ProgramsSem 5 Chapter 16 ProgramsSem 7 Own Topic Program 2  | \*Lab 11.10.2 – Design and Implement a VLSM Addressing Scheme | Module 13 Work |

**February 27 – March 3**